

A

A

U\_POWER  
POWER.SchDoc

U\_CLOCK\_GENERATOR  
CLOCK\_GENERATOR.SchDoc

U\_CONNECTIONS  
CONNECTIONS.SchDoc

U\_FPGA  
FPGA.SchDoc

U\_STM32  
STM32.SchDoc

U\_AUDIO CODEC  
AUDIO CODEC.SchDoc

B

B

U\_DAC  
DAC.SchDoc

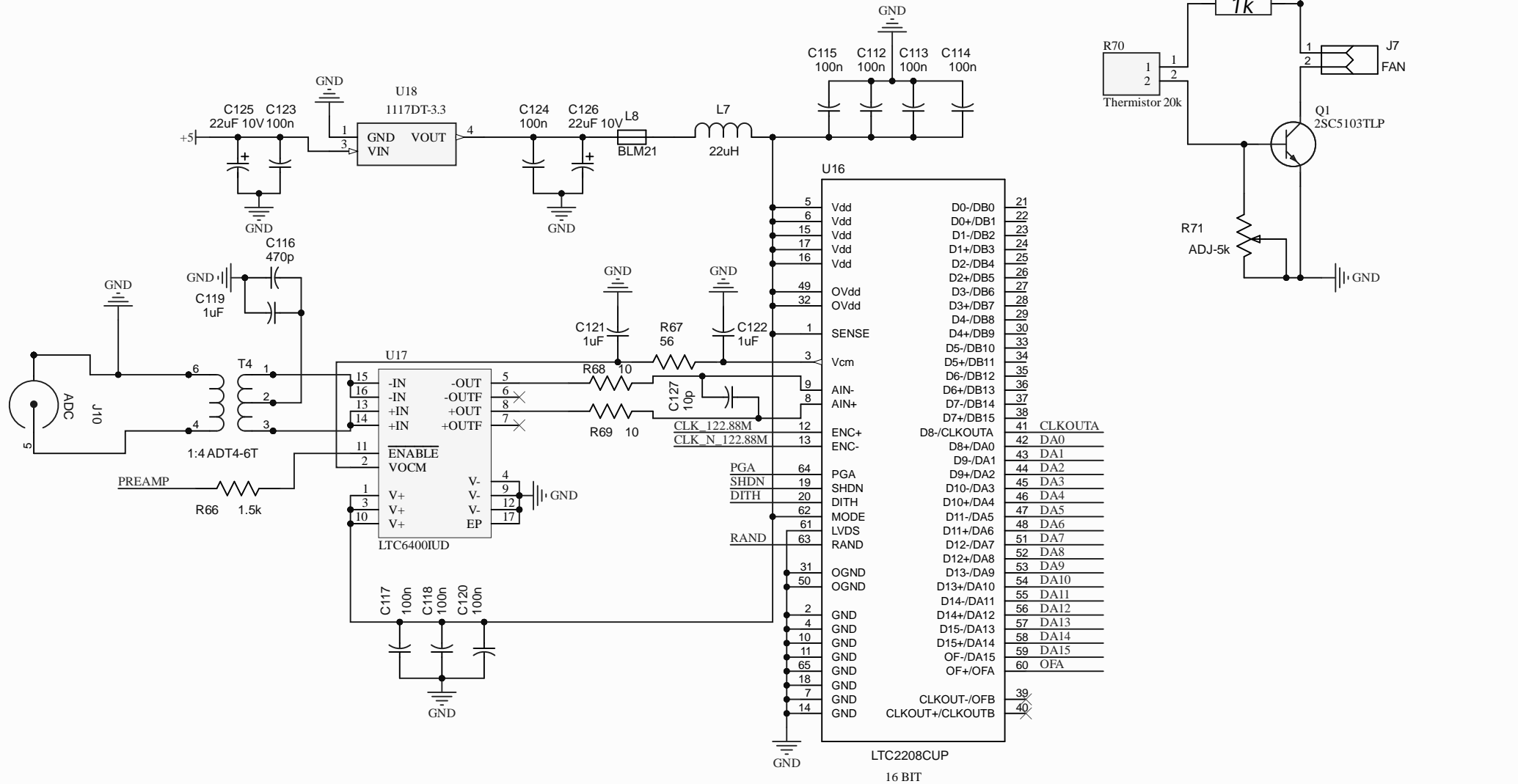
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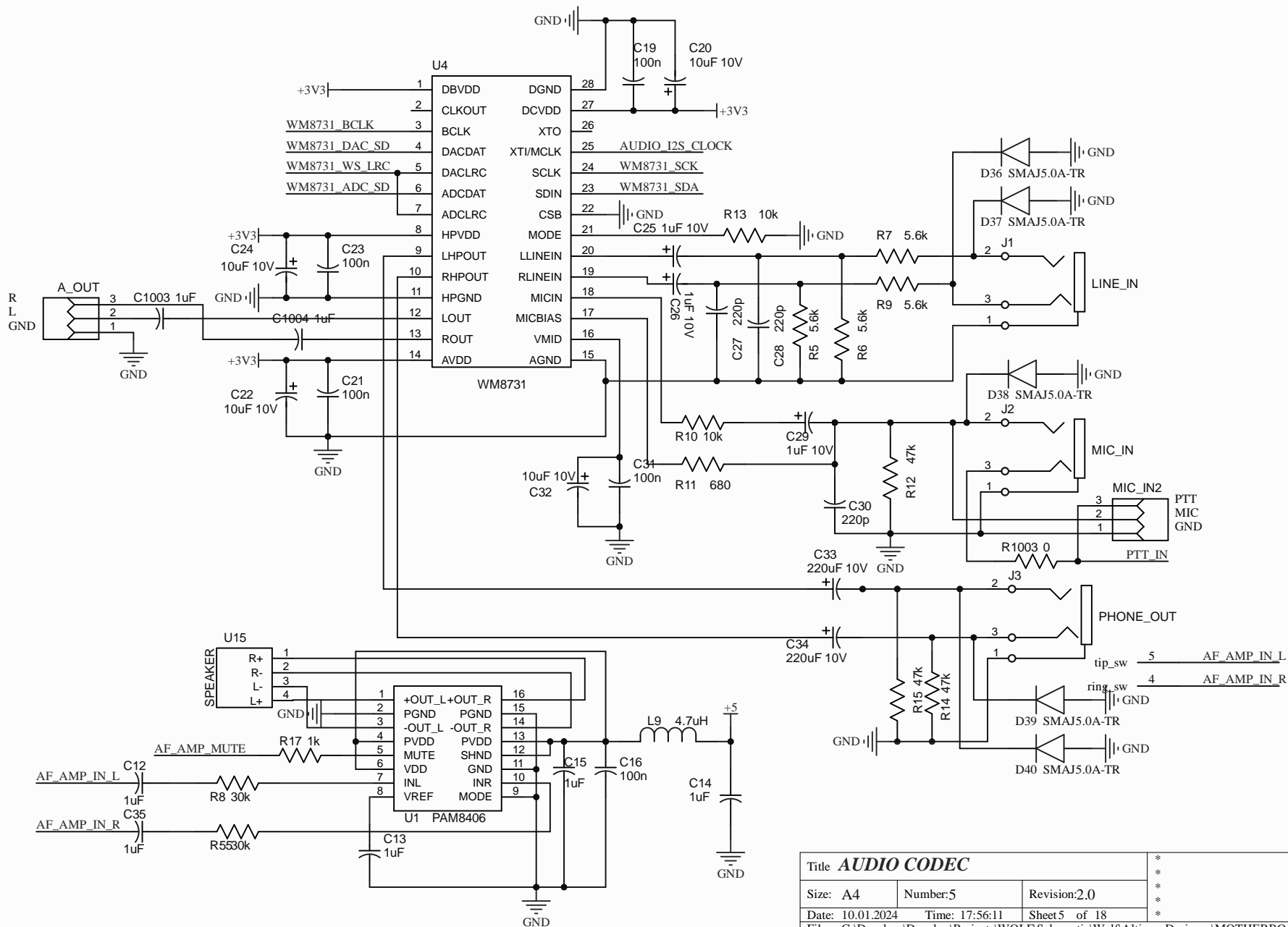
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Number:5

Revision:2,0

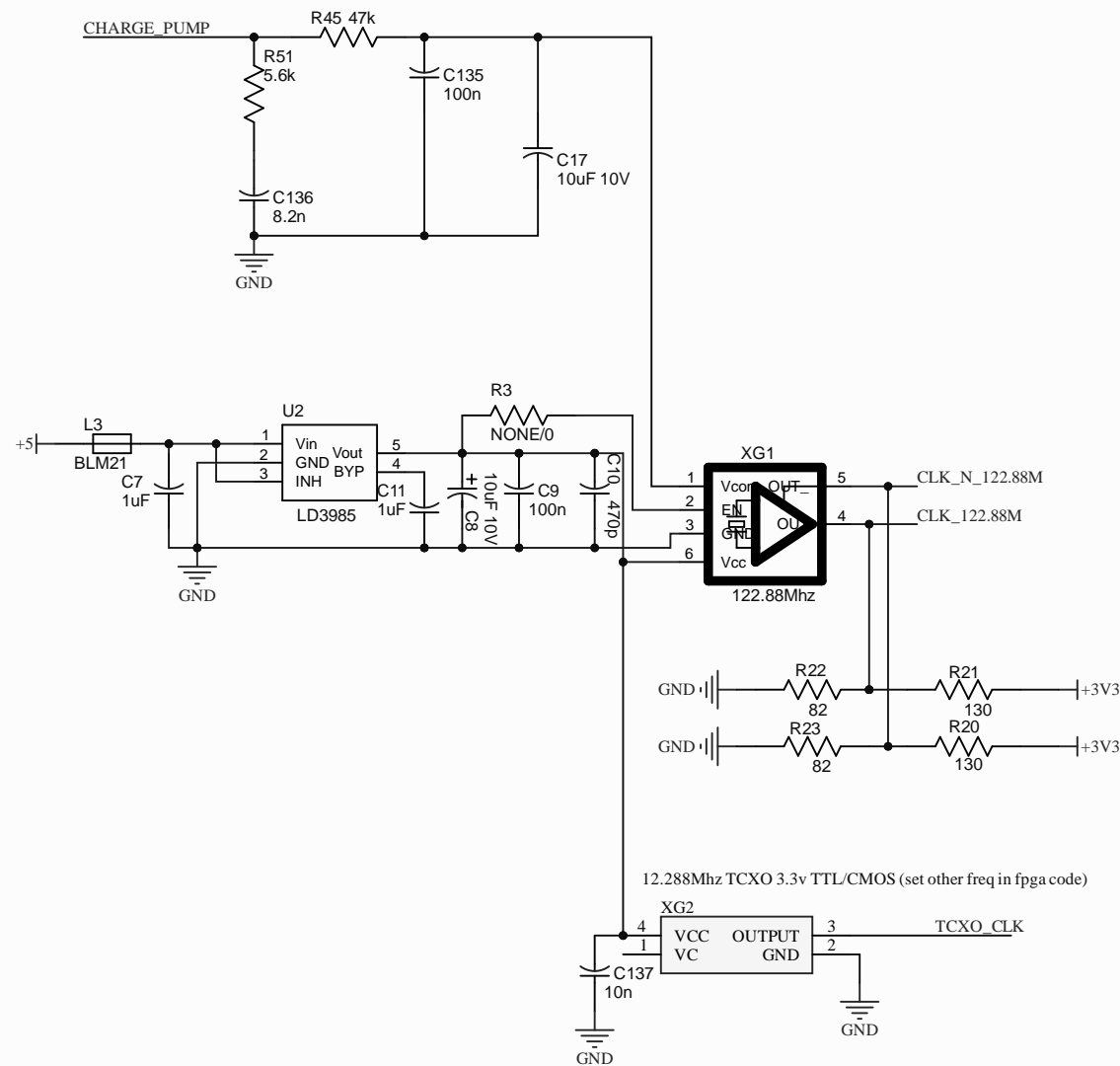
Date: 10.01.2024

Time: 17:56:11

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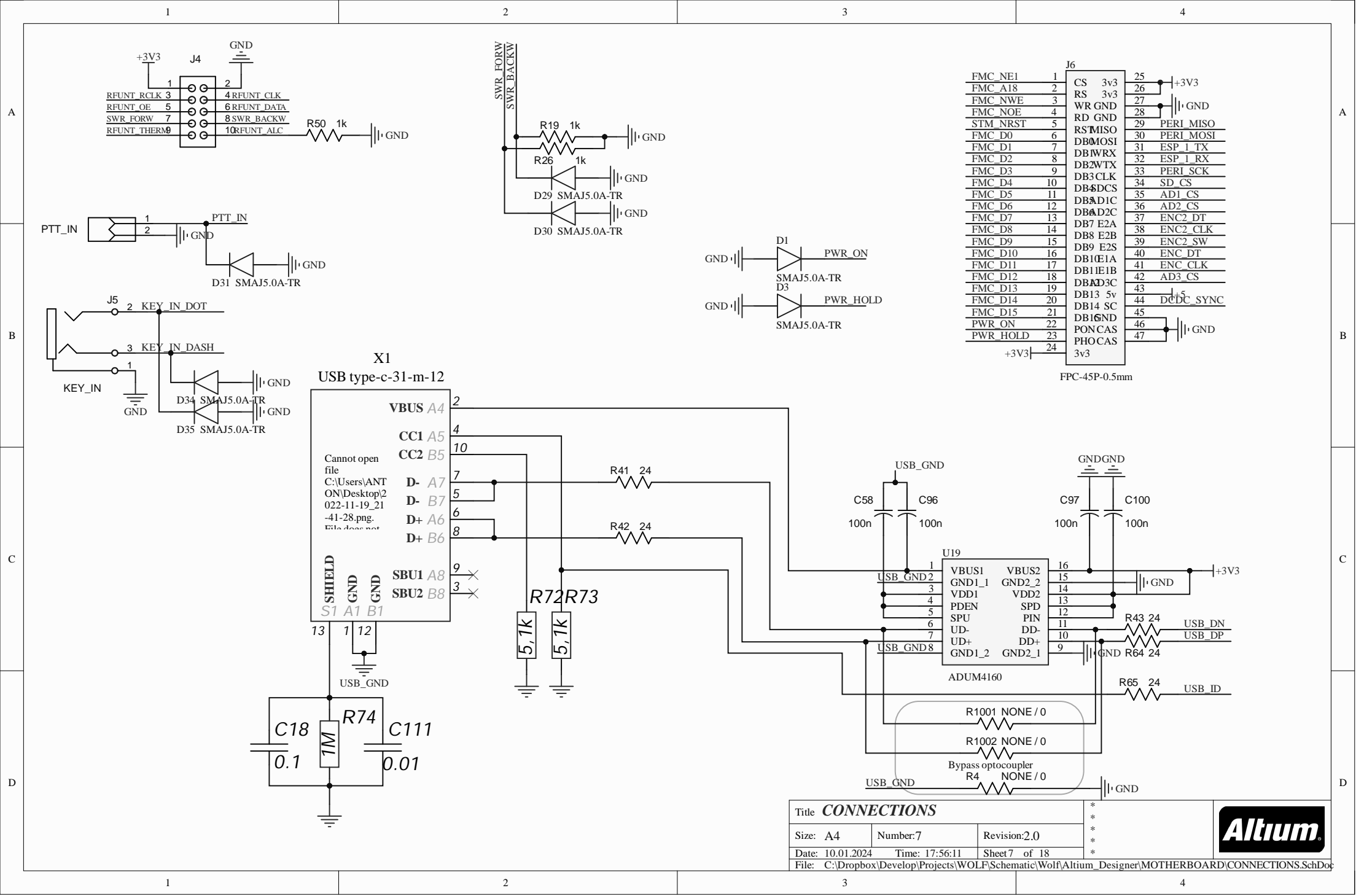
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Size: A4 Number:6 Revision:2,0

Date: 10.01.2024 Time: 17:56:11 Sheet 6 of 18

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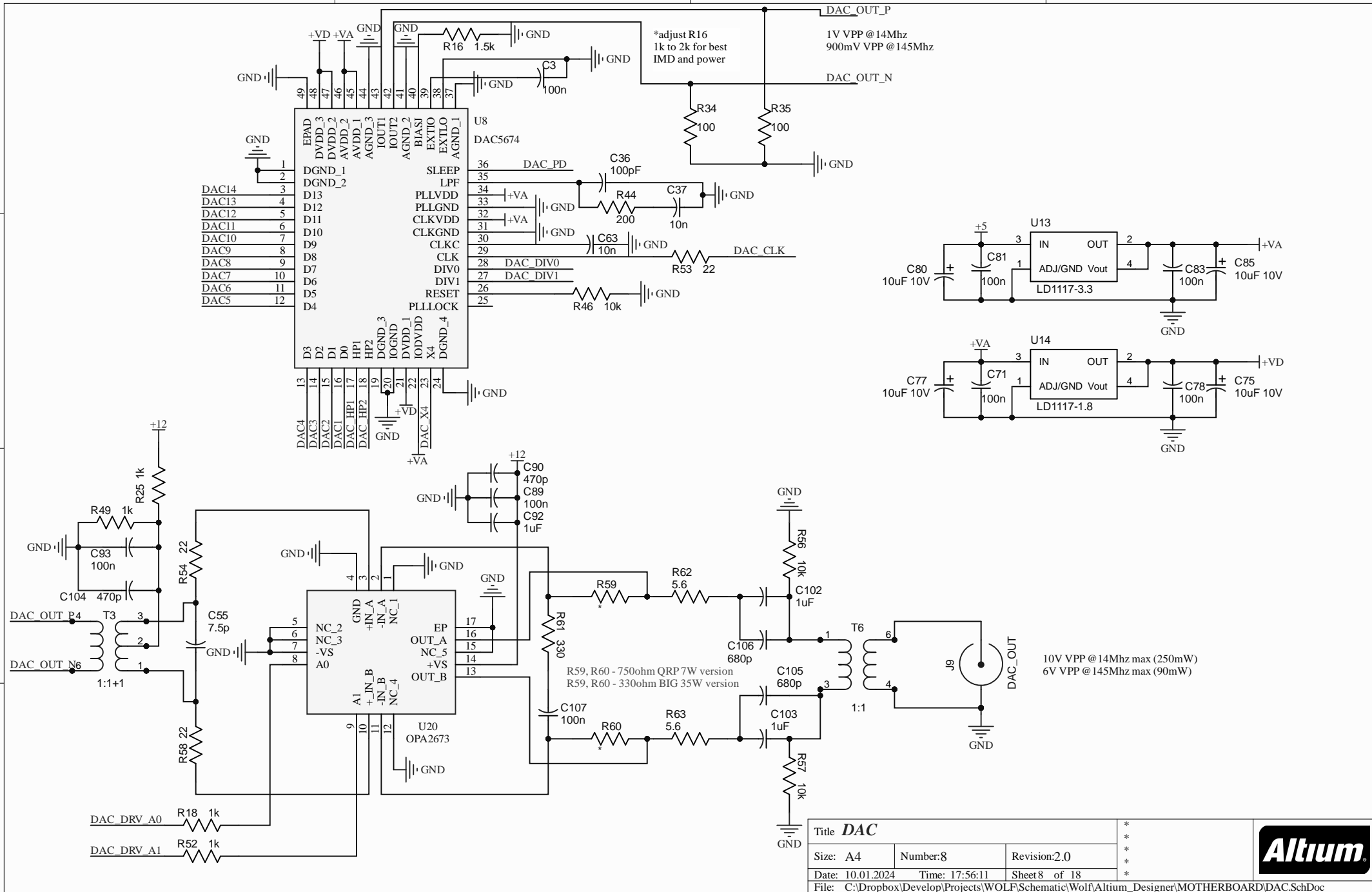
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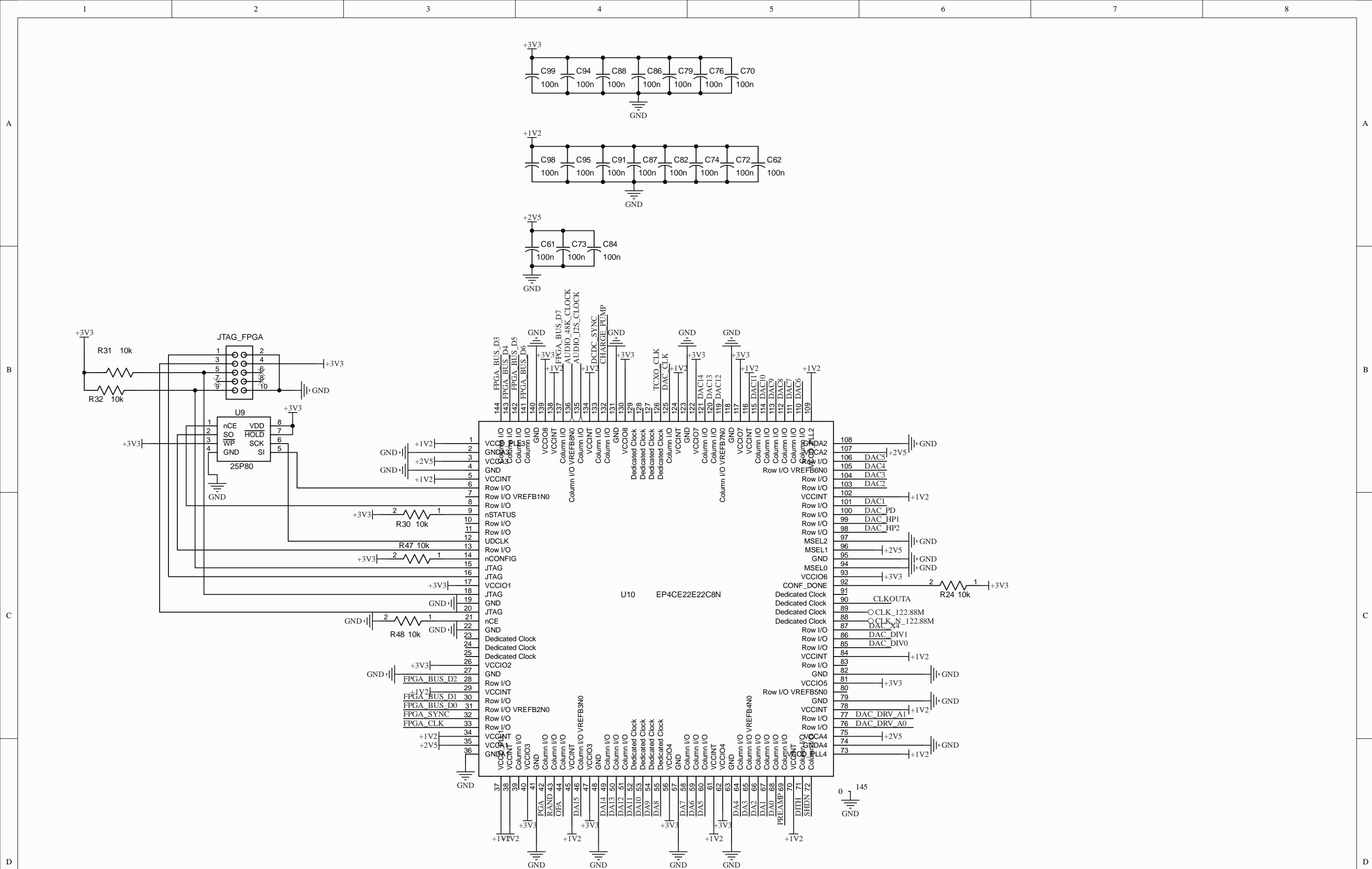
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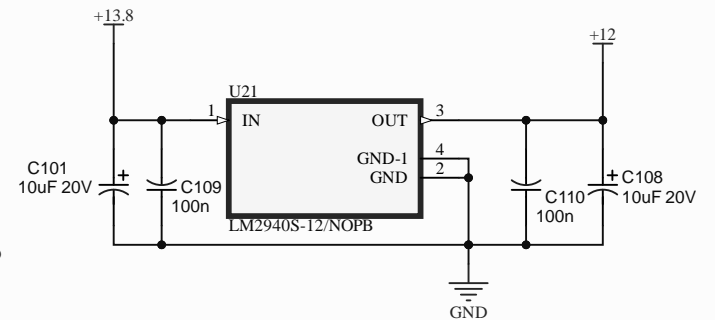
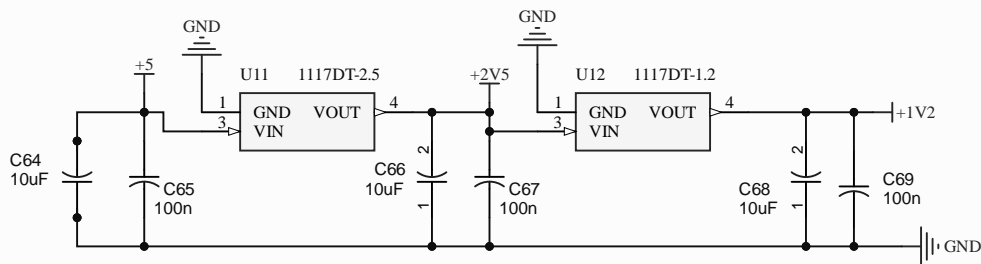
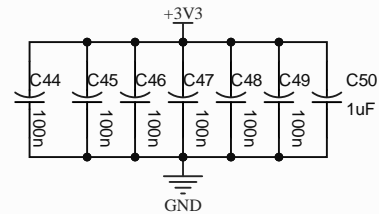
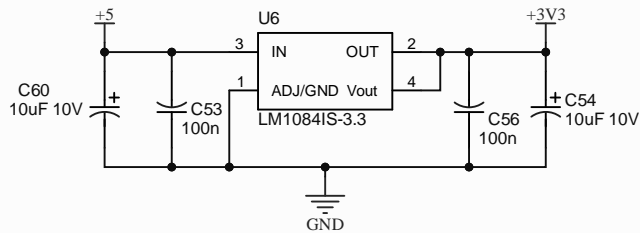
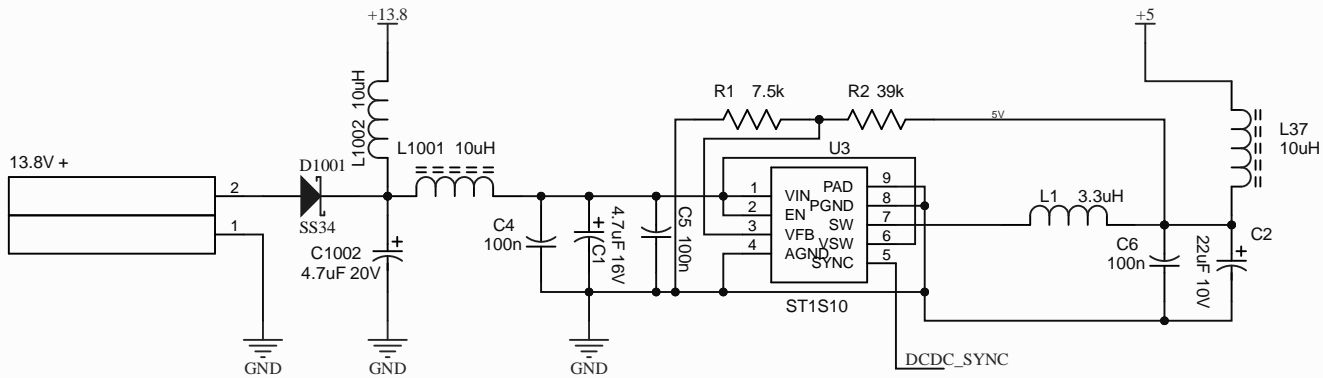
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Title **POWER**

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Number: 10

Revision: 2.0

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